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Reg. No. :

Question Paper Code : 11322

B.E./B.Tech. DEGREE EXAMINATION, APRIL/MAY 2011
Sixth Semester
Electrical and Electronics Engineering
EE 2354 — MICROPROCESSORS AND MICROCONTROLLERS
(Regulation 2008)

Time : Three hours Answer ALL questions Maximum : 100 marks

PART A — (10 × 2 = 20 marks)

1. Specify the size of data, address, memory word and memory capacity of 8085 microprocessor. (8)
2. How is the memory segment accessed by 8086 microprocessor identified? (8)
3. State the function of given 8085 instructions: JP, JPE, JPO, JNZ. (8)
4. How is PUSH B instruction executed? Find the status after the execution. (8)
5. What are the different ways to end the interrupt execution in 8259 programmable Interrupt controller? (8)
6. What is the function of Scan section in 8279 programmable keyboard/Display Controller? (8)
7. List the alternative functions assigned to Port 3 pins of 8051 microcontroller. (8)
8. Mention the size of DPTR and Stack Pointer in 8051 microcontroller. (8)
9. What is the operation of the given 8051 microcontroller instructions: XRL A, direct? (8)
10. What are the different operations performed by Boolean variable instructions of 8051? (8)

PART B — (5 × 16 = 80 marks)

11. (a) (i) Explain the architecture, data flow and instruction execution of 8085 microprocessor. (8)
(ii) With timing diagram, explain the memory read operation in 8085 microprocessor. (8)

Or

(b) (i) Show the pin configuration and function of signals of 8086 microprocessor. (8)
(ii) Show the memory organization and interfacing with 8086 microprocessor. Explain how the memory is accessed. (8)

12. (a) (i) Describe the instruction format and addressing modes of 8085 microprocessor. (8)

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