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#Markus Jensen



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#Diego Butler



so many fake sites. this is the first one which worked! Many thanks

Scheme - G
Sample Question Paper

Course Name : Computer Engineering Group	
Course Code : C00CM1CD/IF/CW	
Semester : Third	17333
Subject Title : Digital Techniques	
Marks : 100	Hours: 3 Hrs.

Q-1) (A) Attempt any SIX: 12 Marks

- Define with respect to digital ICs.
 - Propagation delay
 - Noise immunity
- What is positive logic with respect to a digital signal?
- Draw the logical symbol, Truth table, and logical expression of AND gate.
- State any four Boolean Laws.
- Solve the following:
 - $(110101)_2 + (011101)_2$
 - $(1010)_2 - (1000)_2$ using 1's complement method.
- Draw the symbol, logical expression and truth table of 3:1 OR gate.
- Draw the truth table of digital comparator IC 7485.
- Define the following with respect to DAC
 - Resolution
 - Settling time.

Q-1) (B) Attempt any TWO: 08 Marks

- Convert the following
 - $(212)_{10} = (?)_7$
 - $(1100101)_2 = (?)_{10}$
 - $(436)_8 = (?)_7$
 - $(206)_8 = (?)_{16}$
- State and prove De Morgan's theorem.
- Perform the following BCD arithmetic:
 - $(247)_{10} + (463)_{10}$
 - $(42)_{10} - (27)_{10}$

Q-2) Attempt any FOUR: 16 Marks

- Implement AND & OR gates using NAND gates only.
- Given $Y = AB + BC = \bar{A}C$
Implement the logical expression using gates.
- Perform the following binary operation
 - 11010×1011
 - $11011 - 110$
- Design a Half Adder Circuit.
- Minimize the following Boolean expression using K-map.

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